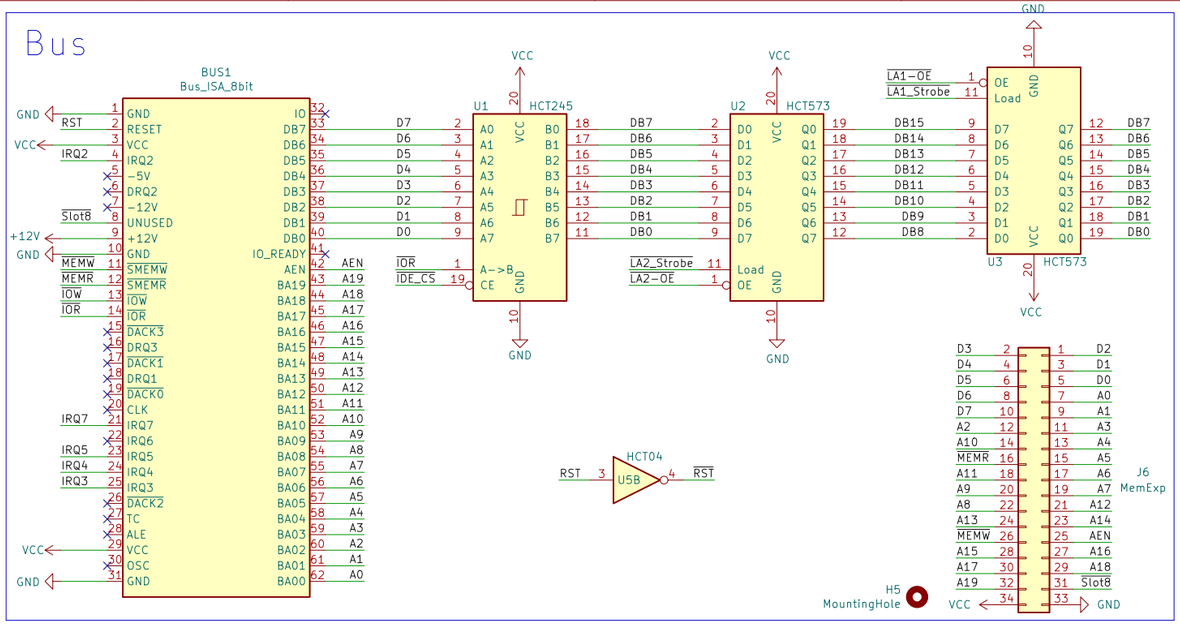
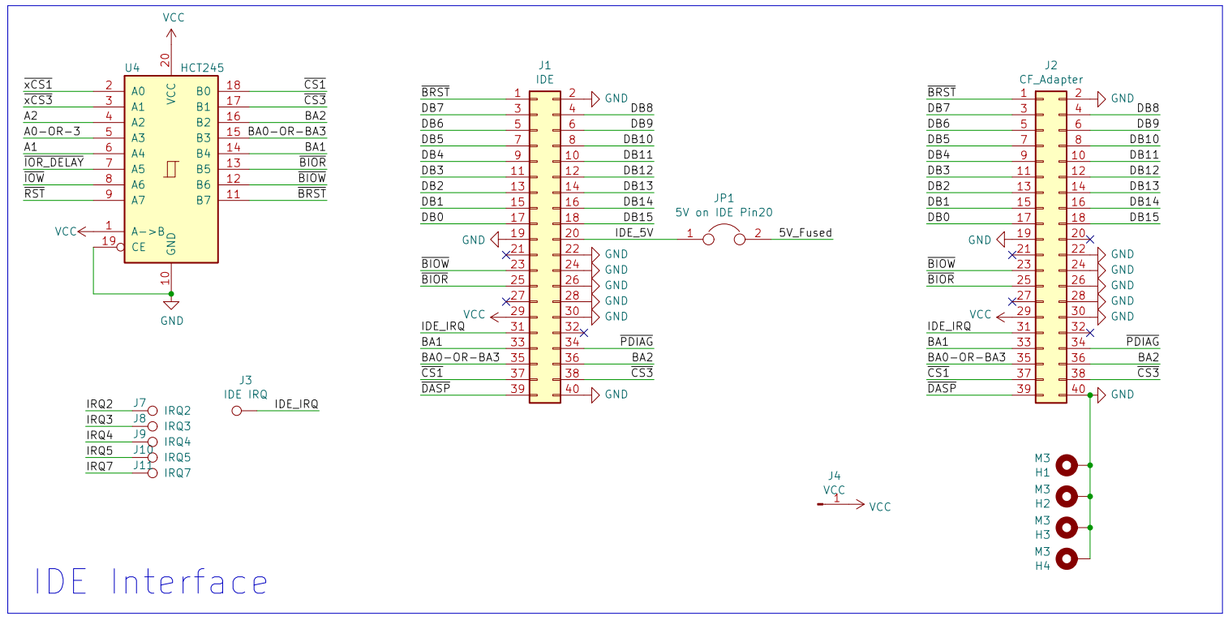
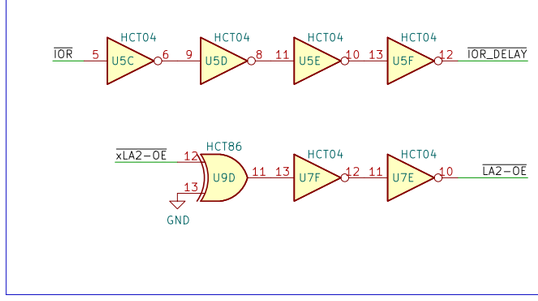
Bus



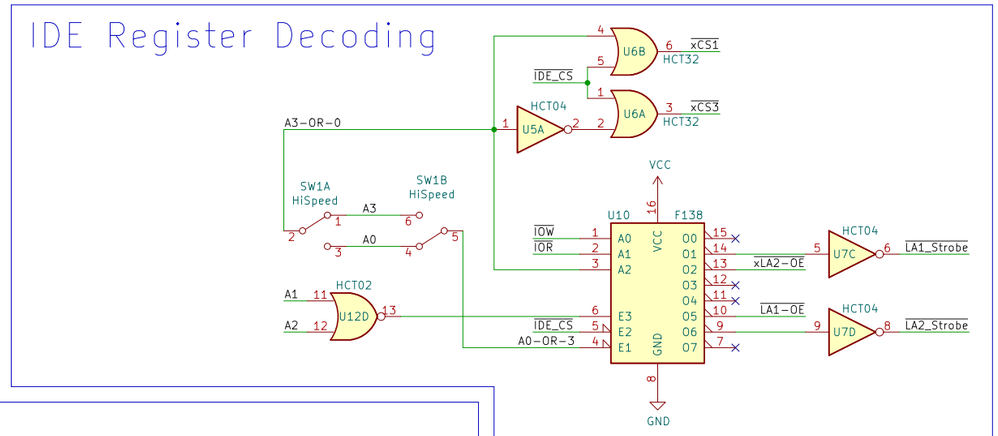
IDE Interface



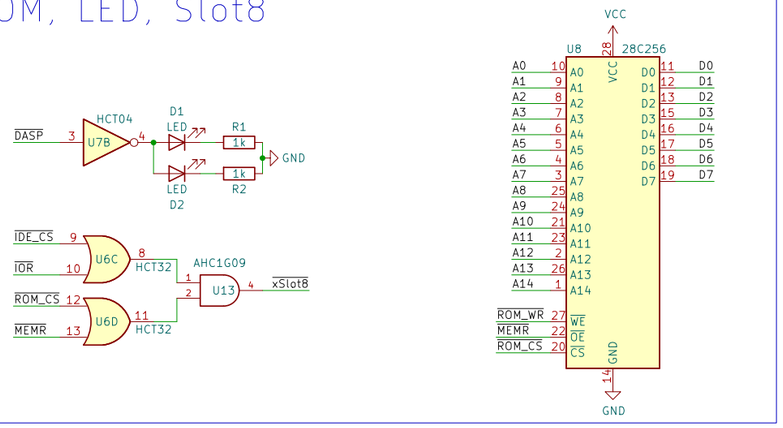
Delay lines



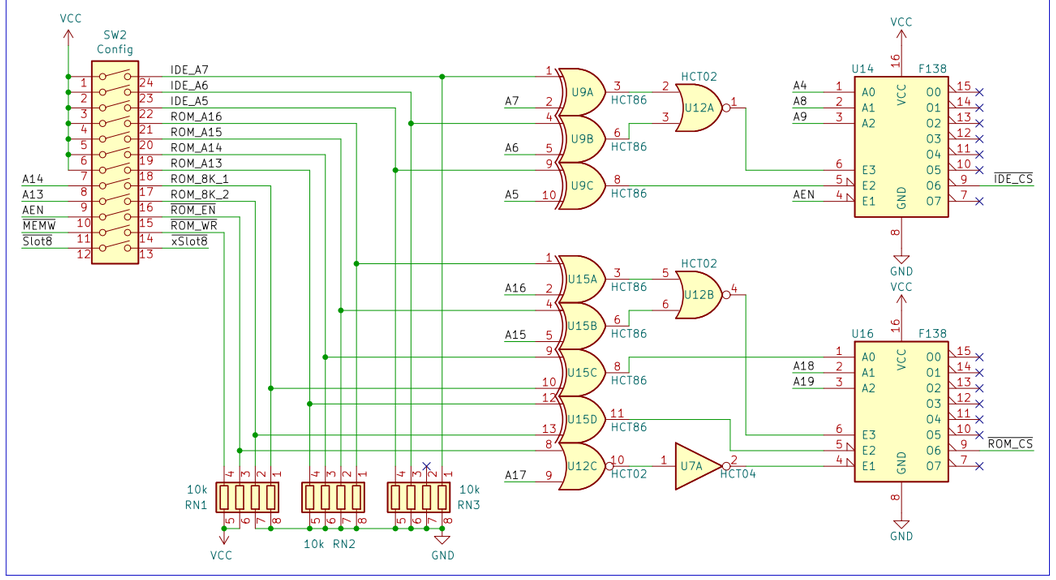
IDE Register Decoding



ROM, LED, Slot8



Address Decoding



Settings

IDE Address:
123 IDE
000 300 !
001 320
010 340
011 360
100 380
101 3A0
110 3C0
111 3E0

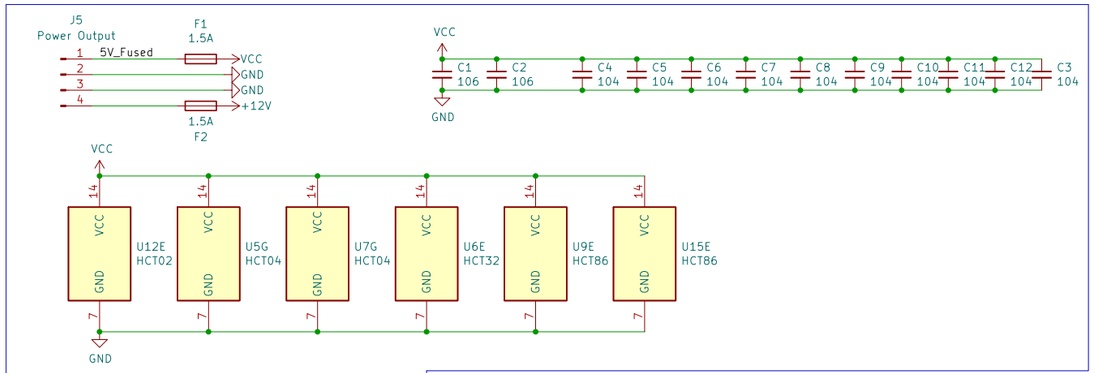
ROM Address:
4567 ROM
0000 C0000*
0001 C2000
0010 C4000 ^
0011 C6000
0100 C8000*
0101 CA000
0110 CC000 ^
0111 CE000
1000 D0000* !
1001 D2000
1010 D4000 ^
1011 D6000
1100 D8000*
1101 DA000
1110 DC000 ^
1111 DE000

! = Default
* = All ROM support
^ = 16K/8K ROM support
= 8K ROM support

ROM Size:
89
11 = 8K: 27(C)64, 28C64
10 = 16K: 27C128
00 = 32K: 28C256 (not 27C256)

10: ROM Enable
11: ROM Write Enable
12: IBM XT Slot-8 Mode

Misc



Based on XT–IDE Rev 2: www.vcfed.org/forum/showthread.php?29202–XTIDE–Rev2
And Glitchworks' XT–IDE: github.com/glitchwrks/xt_ide

Monotech PCs

Sheet: /
File: XT–IDE Deluxe.kicad_sch

Title: XT–IDE Deluxe

Size: A3	Date: 2020–10–21	**Rev: 1.1**
KiCad E.D.A. eeschema 7.0.6		Id: 1/1